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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Document Number: 7594.10

Application of

Dusty Lutz et al.

Serial No. 10/038,377

Filed: October 19, 2001

For: **CACHE AFFINITY SCHEDULING METHOD FOR MULTI-PROCESSOR
NODES IN A SPLIT TRANSACTION BUS ARCHITECTURE**

Assistant Commissioner
for Patents

Washington, D.C. 20231

**NOTICE TO SUBMISSION OF
MISSING PARTS OF REISSUE
PATENT APPLICATION**

Group Art Unit: 3652

CERTIFICATE OF MAILING

(37 CFR 1.8a)

I hereby certify that this correspondence is, on the date shown below, being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

JUN 04 2002

Date

Patricia Carthey
Patricia Carthey

RECEIVED

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GROUP 360

ATTN: Application Processing Division
Special Processing and Correspondence Branch

Sir:

Applicant hereby submits signed formal application
papers to complete the file with respect to the above-referenced
application.

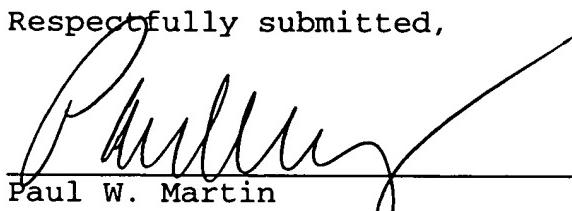
Applicant has not received a Notice to File Missing
Parts of Application.

Applicant hereby authorizes the following fees from
Deposit Account No. 14-0225:

1. Surcharge for Incomplete Application \$ 130.00

Examination on the merits is respectfully requested.

Respectfully submitted,



Paul W. Martin
Attorney for Applicant
Reg No. 34870
Tel. No. (937) 445-2990

JUN 04 2002
Dayton, Ohio